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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control no. Attorney Docket No. 10990265-1 UTILITY **=** ~ **PATENT APPLICATION** First Inventor or Application **DAVID SCOTT TAUBMAN** Identifier. TRANSMITTAL Title APPARATUS FOR AND METHOD OF SNR SCALABLE QUA y for new nonprovisional applications under 37 CFR 1.53(b)) **BIT-PLANE CODING** Express Mail Label No. EL518741559US ADDRESS TO: **Assistant Commissioner for Patents** APPLICATION ELEMENTS **Box Patent Application** Washington, D.C. 20231 See MPEP chapter 600 concerning utility patent application contents. 1. X Fee Transmittal Form 6. \_\_ Microfiche Computer Program (Appendix) (Submit an original, and a duplicate for fee processing) 2. X Specification [Total Pages 23 ] Nucleotide and/or Amino Acid Sequence Submission (preferred arrangement set forth below) (if applicable, all necessary) - Descriptive title of the Invention Computer Readable Copy a. - Cross References to Related Applications b. Paper Copy (identical to computer copy) - Statement Regarding Fed sponsored R & D Statement Verifying identity - Reference to Microfiche Appendix - Background of the Invention - Brief Summary of the Invention - Brief Description of the Drawings (if filed) **ACCOMPANYING APPLICATION PARTS** - Detailed Description Assignment Papers (cover sheet & document(s)) - Claim(s) 37 CFR 3.73(b) Statement - Abstract of the Disclosure (when there is an assignee) Power of Attorney Drawing(s) (35 USC) 113) English Translation Document (if applicable) [Total Sheets 16] 10. Copies of IDS Oath or Declaration [Total Pages 3 ] 11. X Information Disclosure a. X Newly executed (original or copy) Statement (IDS)/PTO-1449 Citations 12. \_ Copy from a prior application (37 CFR 1.63(d)) **Preliminary Amendment** (for continuation/divisional with Box 17 completed) 13. <u>2</u> Return Receipt Postcard (MPEP 503) [Note Box 5 below] (Should be specifically itemized) i. \_\_\_ DELETION OF INVENTOR(S) Small Entity \_\_\_ Statement filed in prior application, 14. \_\_\_ Signed statement attached in the prior application, see Statement(s) Status still proper and desired 37 CFR 1.63(d)(2) and 1.33(b). Certified Copy of Priority Document(s) 15. \_\_\_ Incorporation by Reference (useable if Box 4b is checked) (if foreign priority is claimed) The entire disclosure of the prior application, from which a copy 16. Other: of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein. 17. Ita CONTINUING APPLICATION, check appropriate box and supply the requisite information: Continuation Continuation-in-part (CIP) of prior application No.: Divisional Prior application information: Examiner Group/Art Unit 18. CORRESPONDENCE ADDRESS **Customer Number or Bar Code Label** <u>X</u> Correspondence address below (Insert Customer No. or Attach bar code label here) ATTN: MARC P. SCHUYLER NAME **HEWLETT PACKARD COMPANY IP ADMINISTRATION ADDRESS POST OFFICE BOX 272400 FORT COLLINS** STATE **COLORADO** ZIP CODE 80527-2400 CITY **COUNTRY** (650) 857-3359 FAX USA **TELEPHONE** (650) 852-8063

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## APPARATUS FOR AND METHOD OF SNR SCALABLE QUAD-TREE BIT-PLANE CODING

#### **BACKGROUND OF THE INVENTION**

#### 1. Field of Invention

The present invention relates generally to the field of computer coding and pertains more particularly to coding the bit-planes of an array of numbers.

#### 2. Discussion of the Prior Art

With the advent of the computer and the Internet, man is living increasingly in an information age. The storage and transmission of information or data has taken on a heightened importance. Unfortunately, storage space and transmission bandwidth are not unlimited. Modern computer systems have become increasingly graphics intensive.

Dedicated special purpose memories and hardware have been developed to meet this need.

New and improved graphics file formats and communication protocols are being developed as well. As graphics technologies and products become more prevalent in consumer areas, slim profit margins are demanding the utmost efficiencies. Compression is necessary to achieve efficiency in database representation to conserve disk storage, system memory, cache memory, and special purpose memories. Additional savings can be achieved by conserving bus bandwidths, which becomes more critical with unified computer architectures where graphics and system memory are one and the same.

Data compression is often used to reduce the cost of storing large data files on computers as well as to reduce the time for transmitting large data files between computers. Several compression techniques exist. One technique is to transform the data from its original form into a second form which is easier to manipulate. In the so-called "transform

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methods" data is transformed into coefficients that represent the data in a frequency domain. Coefficients may be quantized without significantly affecting the quality of data that is reconstructed. This is a form of lossy compression. Lossy compression refers to compression with some effect on the rendered output. Redundancy in the coefficients may then be reduced or eliminated without affecting the quality of the reconstructed data. This is a form of lossless compression. Lossless compression refers to compression with no effect on the rendered output.

One well known class of transforms are Wavelet transforms. The Wavelet transforms may be used to perform subband decomposition and produce coefficients that describe the data in a hierarchical multiscale representation. Wavelet transforms have proven useful for the compression of images and the analysis of signals. They have been proposed as the transform for the emerging JPEG-2000 standard.

Among the advantages of the Wavelet transforms is that the transform coefficients can be ordered in a hierarchical structure and transmitted in an "embedded bitstream." The embedded bitstream has a property whereby prefixes of the bitstream yield a continuum of lower rate descriptions of the data at the highest possible levels of quality. If the embedded bitstream is truncated during transmission of image data, for instance, the information already transmitted will allow an entire image to be reconstructed. The quality of the reconstructed image is dependent upon the amount of information transmitted. As additional information is transmitted, the quality of the reconstructed image is improved. If the entire bitstream is transmitted without truncation, a lossless or near-lossless image can be reconstructed. If an embedded bitstream is truncated, a complete image of reduced quality can be reconstructed from the transmitted bits. In contrast, truncation of a non-embedded transmission might only allow several rows of an image to be reconstructed.

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The transmission just described is often referred to as a progressive-by-quality or SNR (signal to noise ratio) scalable image transmission. The coefficients are converted to binary and described by bit-planes. The most significant coefficient bits, that is, the coefficient bits conveying the most important information, are transmitted first. The lesser significant coefficient bits are then transmitted in order of decreasing importance with the least significant coefficient bits being transmitted last.

Another type of transmission is often referred to as progressive-by-resolution transmission. The progressive-by-resolution transmission involves ordering the coefficients according to different levels of image resolution. The different levels are identified by markers in the embedded bitstream. A computer may use the markers to parse the bitstream and transmit the data for the coefficients corresponding to a resolution that is specified by the receiving computer. The receiving computer can then reconstruct an image according to the specified resolution.

A number of coding algorithms have been proposed for ordering the description of the transform coefficients such that the retained number of bits of the description can produce the best quality image. For example, algorithms have been proposed by Shapiro, known as the "Zerotree" algorithm, and an improvement thereon by Said-Pearlman, known as the "SPIHT" algorithm, among others. However, there are a number of problems associated with the Zerotree based algorithms. Among these problems is that Zerotree coders rely on the use of a complex arrangement of lists and pointers. Further, the Zerotree coders minimize image distortion only at bit-plane boundaries. As long as the transmission is truncated at a bit-plane boundary, the image distortion will be minimized. However, if a transmission is truncated at a point other than a bit-plane boundary, the image distortion will not be minimized.

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A definite need exists for a system having an ability to meet the efficiency requirements of graphics intensive computer systems. In particular, a need exists for a system which is capable of ordering the description of the transform coefficients in a simple and fast manner. Ideally, such a system would have a lower cost and a higher productivity than conventional systems. With a system of this type, system performance can be enhanced. A primary purpose of the present invention is to solve this need and provide further, related advantages.

#### SUMMARY OF THE INVENTION

An apparatus for and a method of coding the bit-planes of an array of numbers is disclosed. The values in the array of numbers are converted to binary. The binary values are truncated to a predetermined level of accuracy. The number of bit-planes is determined based on the largest absolute value. A tree structured description of the array is generated based on a modified quad-tree coding technique. An SNR scalable encoding of the significance information for each bit-plane is generated by describing new branches and leaves of the tree corresponding to each bit-plane in a bottom-up-depth-first manner. An encoding of refinement information for each bit-plane is generated. A SNR scalable description of the array is generated by concatenating the encoding of the significance information and the refinement information generated for each bit-plane.

#### BRIEF DESCRIPTION OF THE DRAWING

The above and other objects and advantages of the present invention will be more readily appreciated from the following detailed description when read in conjunction with the accompanying drawing, wherein:

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- FIG. 1 is a block diagram of a coding system according to one embodiment of the present invention;
- FIG. 2 is a graphical representation of a hierarchical subband decomposition of a sixteen pixel by sixteen pixel image;
  - FIG. 3 is an example of a square array representing subband SB8 from FIG. 2;
- FIG. 4 is a table illustrating the binary conversion of the decimal values of the non-zero coefficients from FIG. 3;
- FIG. 5A is an example of a square array of significance information for the highest bit-plane of the coefficients from FIG. 3;
- FIG. 5B is a quad-tree description of the square array of significance information of FIG. 5A;
- FIG. 5C is a table illustrating the encoding of the significance information and the refinement information for FIGS. 5A and 5B;
- FIG. 6A is an example of a square array of significance information for the second highest bit-plane of the coefficients from FIG. 3;
- FIG. 6B is a quad-tree description of the square array of significance information of FIG. 6A;
- FIG. 6C is a table illustrating the encoding of the significance information and the refinement information for FIGS. 6A and 6B;
- FIG. 7A is an example of a square array of significance information for the third highest bit-plane of the coefficients from FIG. 3;
- FIG. 7B is a quad-tree description of the square array of significance information of FIG. 7A;

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FIG. 7C is a table illustrating the encoding of the significance information and the refinement information for FIGS. 7A and 7B;

FIG. 8A is an example of a square array of significance information for the lowest bitplane of the coefficients from FIG. 3;

FIG. 8B is a quad-tree description of the square array of significance information of FIG. 8A; and

FIG. 8C is a table illustrating the encoding of the significance information and the refinement information for FIGS. 8A and 8B.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Turning first to FIG. 1, a block diagram of a coding system 10 according to one embodiment of the present invention is shown. The coding system 10 operates on an input image 12 which is decomposed using a standard sampling filter 14. Under a Wavelet transform, the filter 14 decomposes the image into four frequency subbands which will be further described below. The output of the filter 14 is termed a Wavelet transform 16 which is processed by an encoder 18 to generate a compressed image 20. The encoder 18 efficiently generates the compressed image 20 using a modified quad-tree coding technique described below. The compressed image 20 may be decompressed using a decoder 22. The decoder 22 similarly utilizes an inverse of the modified quad-tree coding technique to produce an output image 24.

FIG. 2 shows a graphical representation of a hierarchical subband decomposition of a sixteen pixel by sixteen pixel image. A Wavelet transform may be used to perform the subband decomposition. The filter 14 of FIG. 1 is such an example. At a first level of decomposition, image data, that is, luminance or chrominance data of an image, is filtered by

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high pass and low pass filters, both horizontally and vertically. Then each of the resulting bands is subsampled by a factor of two, both horizontally and vertically. At the first level, the resulting subbands are as follows: a low pass horizontal, low pass vertical subband; a high pass horizontal, low pass vertical subband (SB9); a high pass horizontal, high pass vertical subband (SB8); and a low pass horizontal, high pass vertical subband (SB7). Each subband is one-quarter the size of the original image. In this case, at the first level, each subband will contain sixty-four coefficients arranged in a square array or matrix pattern.

Each additional decomposition is performed on the low pass horizontal, low pass vertical subband generated at the previous level of decomposition. The results are the SB4-SB6 subbands as shown and a new low-low band. In this case, at the second level, each subband will contain sixteen coefficients arranged in a square array. Further, the third decomposition would be performed on the new low-low band. The results are the SB0-SB3 subbands as shown. In this case, at the third level, each subband will contain four coefficients arranged in a square array. Three levels of decomposition are performed to produce the three-level subband decomposition shown. The resulting array of coefficients in this case is sixteen by sixteen having 256 entries. In actual practice, the number of levels of decomposition will depend on the image size with the larger the image the larger the number of decompositions and the larger the array of coefficients. The resulting array quickly becomes unwieldy for manual analysis.

The coarsest frequency information may be found in the lowest frequency subband SB0. The finest frequency information may be found in the highest frequency subband SB8. Parent-child dependencies are shown with the arrows pointing from the subband of the parent nodes to the subband of the child nodes. In a Wavelet transform, the magnitude of the coefficients become smaller as the frequency increases.

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With reference to FIG. 3, an example of a square array representing subband SB8 from FIG. 2 is shown. The square array is filled with example coefficients and the rows and columns are labeled 1-8 from top to bottom and from left to right, respectively. In general, a coefficient will be represented as  $c_{i,j}$ , where i is the row and j is the column. For example, coefficient  $c_{I,4}$  is shown to have a value of 6 in the array. Upon review, one will realize that many of the entries have zero values. This is simply for convenience and will not necessarily be the case. The method below will be illustrated with respect to this square array, but one of ordinary skill will realize that the method can be expanded to any size square or rectangular array as necessary. Rectangular arrays are handled by extending the rows and columns with zero entries until a square array is obtained.

Quad-tree coding is a well known technique for generating a tree structured description of a square array having binary entries. The tree is described from the root at the top to the leaves at the bottom. At depth one or the root, there is only one node. The root node corresponds to the whole square array. From depth one down, the tree repetitively splits each node into four nodes at each successively lower depth. So, at depth two, there are four nodes. At depth three, there are four, eight, twelve, or sixteen nodes depending on whether or not each node at depth two split. Not every node will split at the next depth. This splitting continues until the description is complete. The size and shape of the tree will depend in part on both the size of the square array and the values of its entries. Except for the root, these nodes are either branches or leaves. Branches correspond to four equal sized row-column aligned quadrants or sub-blocks of the square array. Branches may also be referred to as internal nodes. Only those nodes corresponding to sub-blocks containing at least one non-zero entry have branches. Leaves correspond to sub-blocks that have all zero entries in the square array or, at the lowest depth, to sub-blocks of individual entries of the square array.

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The lowest level leaves are labeled with the values of their corresponding array entries. The tree is typically described in a depth first fashion, starting from the root, by specifying whether the new node is a branch or a leaf.

One of ordinary skill in the art will realize that computers generally operate in binary. As a result, decimal numbers have to be converted to binary. In general, the binary representation of a decimal number will take the form of  $b^{m-1}b^{m-2}...b^nb^{n-1}...b^lb^0.b^{-1}b^{-2}...$  where each bit b is either one or zero. For example, the decimal number 10 converts to 1010 in binary ( $b^3b^2b^1b^0$ ). In this case, m is equal to four because the binary representation has four bits (4-1=3). This represents full precision. If one did not need full precision, some of the bits could be ignored. For example, say one was limited to bits  $b^3$  and  $b^2$  in this case, still one would know that the decimal number was 8, 9, 10, or 11. In certain applications this may be enough precision. In return for less precision, the number of bits was cut in half in this case.

FIG. 4 shows a table illustrating the binary conversion of the decimal values of the non-zero coefficients from FIG. 3. One of ordinary skill will realize that for a full conversion, the sign will also need to be converted using the binary alphabet. This has been omitted for convenience. Each number has been converted using the number of bits needed for conversion of the maximum coefficient. In this case, coefficient  $c_{1,1}$  has the maximum absolute value and requires four bits. As a result, even though coefficient  $c_{4,2}$  only requires one bit, it is shown by way of four bits with leading zeros. Implicit in the illustration is that the precision was cut off at the decimal place. In general, the precision could be cut off at any bit position on either side of the decimal point. Zero bits greater than  $b^3$  and less than  $b^0$  have been ignored for convenience.

Taken together across the binary conversions, the bits at a given precision are called bit-planes. For example, the bits for b<sup>3</sup> are grouped into bit-plane three. Quad-tree coding for

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generating a tree structured description of a square array having binary entries is applied to these bit-planes. The tree, represented by  $T_n$  where n is the number of the bit-plane, describes significance information as to which coefficients are significant relative to a particular bitplane. The bit-planes are considered from highest to lowest. The significance information for a coefficient  $c_{i,j}$  is represented by  $s^n(c_{i,j})$ . A coefficient is said to be significant, that is,  $s^n(c_{i,j}) = 1$ , at a particular bit-plane n if one of two conditions exist. First, if the coefficient has a value of one for the first time at that bit-plane, that is, the coefficient had leading zeros in all of the higher bit-planes m-1 through n+1. Second, if the coefficient had a value of one in any of the higher bit-planes, that is, the coefficient remains significant at all bit-planes below its initial significance. Otherwise, the coefficient  $c_{i,j}$  is insignificant, that is,  $s^n(c_{i,j}) = 0$ . From the definition of significance it follows that once a coefficient becomes significant at a particular bit-plane it remains significant at all lower bit-planes. This means that for all bitplanes n,  $T_{n-1}$ , the quad-tree description of the significance information for bit-plane n-1, can be obtained by adding branches and leaves to the leaves corresponding to zero regions, that is, zero-labeled leaves, of  $T_{n-1}$ . In this sense,  $T_{n-1}$  is a refinement of  $T_n$ . The encoding of the manner in which  $T_n$  should be refined to obtain  $T_{n-1}$  is represented by  $\Delta_n$ . The sign of the corresponding coefficient is encoded with the first significant bit for that coefficient using a one to represent a negative sign and a zero to represent a positive sign. At the highest bitplane m-1, the significance information corresponds to the values of the coefficients for that bit-plane. For each bit-plane below the highest bit-plane, the values of the coefficients which became significant at higher bit-planes constitute refinement information and the encoding of this information is represented by  $R_n$ .

An SNR scalable bit-stream for a square array of coefficients is obtained by concatenating the significance information  $\Delta_n$  and the refinement information  $R_n$  in the order

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 $\Delta_{m-1} R_{m-1} \Delta_{m-2} R_{m-2} \dots \Delta_n R_n \Delta_{n-1} R_{n-1} \dots \Delta_1 R_1 \Delta_0 R_0 \dots \Delta_1 R_{-1} \Delta_2 R_{-2} \dots$  (1)

where  $R_{m-1}$  is always an empty set. This order is valuable because it allows some image to be generated even if the transmission is truncated during the transfer of the bit-stream. For best application of the quad-tree technique to bit-plane coding, attention should be paid to the manner in which a higher tree  $T_n$  is refined to obtain a lower tree  $T_{n-1}$ , the description of which constitutes  $\Delta_{n-1}$ . The order of the description can have a significant effect on the smoothness of the distortion rate behavior of the final SNR scalable bit-stream. The refinement of  $T_n$  should be guided by what shall be called the embedding principle, that is, all bit-plane information should be described in order of decreasing expected distortion reduction per expected bit of description. Based on this principle, it can be shown that the result is that the significance tree should be described in a bottom-up-depth-first fashion. This means that in encoding the refinement of  $T_n$  into  $T_{n-1}$ , the new values of the maximum depth zero-labeled leaves of  $T_n$  are described first (including the signs of the newly significant coefficients). Then each new sub-tree, having new branches and leaves, growing out of each shallower zero-labeled leaf of  $T_n$  is described in a depth-first fashion. The shallower zero-labeled leaves of  $T_n$  are processed in order of decreasing depth. This procedure will be illustrated below with respect to the example of FIG. 3.

FIG. 5A shows an example of a square array of significance information  $s^n(c_{i,j})$  for the highest bit-plane of the coefficients from FIG. 3. Recall from above that at the highest bit-plane, the significance information corresponds to the values of the coefficients for that bit-plane. So, in this example  $s^3(c_{i,j})$  is equal to  $b^3_{i,j}$ . Because of this, all of the relevant information is contained in the encoding of the significance information  $\Delta_3$  and there is no refinement information  $R_3$  for the highest bit-plane. A quad-tree description of the square array of significance information of FIG. 5A is shown in FIG. 5B. The resulting tree is of

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depth four with some of the leaves at each of the depths two, three, and four. The tree is formed by examining the significance information at various depths. At depth one, the significance information is taken as a whole and is examined to determine if any of the entries are significant. In this case, both  $s^3(c_{1,1})$  and  $s^3(c_{4,3})$  are one indicating that  $c_{1,1}$  and  $c_{4,3}$  are significant. As a result, the root or node 26 is labeled with a one. At depth two, the significance information is broken into four quadrants and each quadrant is examined to determine if any of the entries in that quadrant are significant. In this case, the upper left quadrant has significant entries. As a result, node 28 is labeled with a one and nodes 30-34 are labeled with a zero. At depth three, each significant quadrant from depth two is broken into four sub-quadrants and each sub-quadrant is examined to determine if any of the entries are significant. In this case, the upper left sub-quadrant and the lower right sub-quadrant have significant entries. As a result, nodes 36 and 42 are labeled with a one and nodes 38 and 40 are labeled with a zero. At depth four, each significant sub-quadrant from depth three is broken down into four sub-sub-quadrants and each sub-sub-quadrant is examined to determine if any of the entries are significant. In this case,  $s^3(c_{1,1})$ , the upper left sub-subquadrant of the upper left sub-quadrant, and  $s^3(c_{4,3})$ , the lower left sub-sub-quadrant of the lower right sub-quadrant, are significant. As a result, nodes 44 and 72 are labeled with a one and nodes 46-50, 68, 70 and 74 are labeled with a zero. No further division of the significance information is possible, so the tree is complete. The final tree has root node 26, branch nodes 28, 36, and 42, and leaf nodes 30-34, 38, 40, 44-50, and 68-74. The positive sign [+] of  $c_{I,I}$  has been noted under node 44 and the negative sign [-] of  $c_{4,3}$  has been noted under node 72 as reminders to code the signs accordingly before coding nodes 44 and 72 themselves.

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In FIG. 5C a table illustrating the encoding of the significance information and the refinement information for FIGS. 5A and 5B is shown. Recall from above that the significance tree  $T_n$  should be described in a bottom-up-depth-first fashion. Realize also that for the highest bit-plane m-I the entire tree must be described. For convenience, the tree is described from left to right as drawn. Once described, a node is not redescribed. For instance, the root is only described once but is connected ultimately to all of the leaves. In this case, the order of the description is 26, 28, 36, 44-50, 38-42, 68-74, and 30-34. The resulting encoding of significance information  $\Delta_3$  is shown in the table. Remember that the sign of the corresponding coefficient is encoded with the first significant bit for that coefficient. Here, for example, the binary values of the positive and negative signs have been shown in square brackets for ease of identification, but no such bracket would be transmitted. Recall further from above that  $R_{m-1}$  is always an empty set. So,  $R_3$  is an empty set in this example. Accordingly, after encoding of the highest bit-plane of the coefficients from FIG. 3, the SNR scalable bit-stream consists of  $\Delta_3 R_3$  where  $R_3$  is an empty set.

With reference to FIG. 6A, there is shown an example of a square array of significance information for the second highest bit-plane of the coefficients from FIG. 3. Recall from above that the coefficient remains significant at all bit-planes below its initial significance. So,  $s^2(c_{1,1})$  and  $s^2(c_{4,3})$  are one because  $s^3(c_{1,1})$  and  $s^3(c_{4,3})$ , respectively, were one. In addition, newly significant at this bit-plane is  $s^2(c_{1,4})$  because bit  $b^2$  for coefficient  $c_{1,4}$  is significant. A quad-tree description of the square array of significance information of FIG. 6A is shown in FIG. 6B. The resulting tree is still of depth four with most of the leaves at depth four, one of the leaves at depth three, and three of the leaves at depth two. The tree is formed by examining the significance information at the same depths as FIG. 5B above. At depth one, the significance information is the same as FIG. 5B. As a result, the root is labeled

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with a one. At depth two, the significance information is the same as FIG. 5B. As a result, node 28 is labeled with a one and nodes 30-34 are labeled with a zero. At depth three, the significance information is the same as FIG. 5B with one addition. In this case, the upper right sub-quadrant also has a significant entry. As a result, nodes 36, 38, and 42 are labeled with a one and node 40 is labeled with a zero. At depth four, the upper right sub-quadrant from depth three is broken into four sub-sub-quadrants and each sub-sub-quadrant is examined to determine if any of the entries are significant. In this case,  $s^2(c_{1,4})$ , the upper right sub-sub-quadrant of the upper right sub-quadrant, is one. As a result, nodes 44, 54, and 72 are labeled with a one and nodes 46-52, 56, 58, 68, 70, and 74 are labeled with a zero. The final tree has root node 26, branch nodes 28, 36, 38, and 42, and leaf nodes 46-50, 52, 56, 58, 68, 70, 74, 40, and 30-34. The values for bits  $b^2$  for coefficients  $c_{1,1}$  and  $c_{4,3}$  have been noted under nodes 44 and 72, respectively, for future reference when coding the refinement information.

Turning now to FIG. 6C, a table illustrating the encoding of the significance information and the refinement information for FIGS. 6A and 6B is shown. Because the coefficient remains significant at all bit-planes below its initial significance, the nodes of the tree that are labeled with a one will remain a one. As a result, below the highest bit-plane only the zero labeled nodes of the next higher tree are of interest. The other nodes can be ignored because they are known and have been described previously. In this case, only nodes 46-50, 68, 70, 74, 38, 40 and 30-34 of FIG. 5B are of interest for coding the new status of the leaf nodes of the tree of FIG. 6B. Describing the significance information from left to right and in a bottom-up-depth-first fashion, nodes 46-50 remain unchanged. Nodes 68, 70, and 74 also remain unchanged. Moving up to depth three, node 38 changes to being significant, as a result, nodes 52-58 must be described for the first time. Also, node 40 remains unchanged.

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Moving up to depth two, nodes 30-34 remain unchanged. The resulting order of the description is 46-50, 68, 70, 74, 38, 52-58, 40, and 30-34. The resulting encoding of the new status of these nodes  $\Delta_2$  is shown in the table. Recall from above that for each bit-plane below the highest bit-plane, the values of the coefficients which became significant at higher bit-planes are refinement information and its encoding is represented by  $R_n$ . For convenience, the refinement information is described from left to right. In this case, the values for bits  $b^2$  for coefficients  $c_{I,I}$  and  $c_{4,3}$  make up refinement information  $R_2$  as shown in the table. Accordingly, after encoding of the two highest bit-planes of the coefficients from FIG. 3, the SNR scalable bit-stream consists of  $\Delta_3$   $R_3$   $\Delta_2$   $R_2$ .

FIG. 7A shows an example of a square array of significance information for the third highest bit-plane of the coefficients from FIG. 3. In addition to the coefficients that remain significant from above, newly significant at this bit-plane is  $c_{3,2}$  because bit  $b^I$  for coefficient  $c_{3,2}$  is one and all higher bits are zero. A quad-tree description of the square array of significance information of FIG. 7A is shown in FIG. 7B. The resulting tree is still of depth four with most of the leaves at that depth and three of the leaves at depth two. At depths one and two, the significance information is the same as FIG. 6B At depth three, the significance information is the same as FIG. 6B with one addition. In this case, the lower left sub-quadrant also has a significant entry. As a result, nodes 36-42 are labeled with a one. At depth four, the lower left sub-quadrant from depth three is broken into four sub-sub-quadrants and each sub-sub-quadrant is examined to determine if any of the entries are significant. In this case,  $s^I(c_{3,2})$ , the upper right sub-sub-quadrant of the lower left sub-quadrant, is one. As a result, nodes 44, 54, 62, and 72 are labeled with a one and nodes 46-52, 56-60, 64-70, and 74 are labeled with a zero. The final tree has root node 26, branch nodes 28 and 36-42, and leaf nodes 44-74 and 30-34. As above, the values for bits  $b^I$  for coefficients  $c_{I,I}$ ,  $c_{I,4}$ , and  $c_{4,3}$  have

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been noted under nodes 44, 54, and 72, respectively, for future reference when coding the refinement information.

Turning now to FIG. 7C, a table illustrating the encoding of the significance information and the refinement information for FIGS. 7A and 7B is shown. Recall from above that below the highest bit-plane only the zero labeled nodes of the next higher tree are of interest. In this case, only nodes 46-52, 56, 58, 68, 70, 74, 40, and 30-34 of FIG. 6B are of interest for coding significance information for FIG. 7B. Describing the significance information from left to right and in a bottom-up-depth-first fashion, nodes 46-52, 56, 58, 68, 70, and 74 remain unchanged. Moving up to depth three, node 40 changes to being significant, as a result, nodes 60-66 must be described for the first time. Moving up to depth two, nodes 30-34 remain unchanged. The resulting order of the description is 46-52, 56, 58, 68, 70, 74, 40, 60-66, and 30-34. The resulting significance information  $\Delta_I$  are shown in the table. Because for each bit-plane below the highest bit-plane, the values of the coefficients which became significant at higher bit-planes are encoded as refinement information, in this case, the values for bits  $b^I$  for coefficients  $c_{I,I}$ ,  $c_{I,4}$ , and  $c_{4,3}$  make up the refinement information  $R_I$  as shown in the table. Accordingly, after encoding of the three highest bitplanes of the coefficients from FIG. 3, the SNR scalable bit-stream consists of  $\Delta_3$   $R_3$   $\Delta_2$   $R_2$   $\Delta_1$  $R_1$ .

FIG. 8A shows an example of a square array of significance information for the lowest bit-plane of the coefficients from FIG. 3. In addition to the coefficients that remain significant from above, newly significant at this bit-plane is  $s^0(c_{4,2})$  because bit  $b^0$  for coefficient  $c_{4,2}$  is one. A quad-tree description of the square array of significance information of FIG. 8A is shown in FIG. 8B. The resulting tree is still of depth four with most of the leaves at that depth and three of the leaves at depth two. At depths one, two, and three, the

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significance information is the same as FIG. 7B. At depth four, the four sub-sub-quadrants of the lower left sub-quadrant from depth two are examined to determine if any of the entries are significant. In this case,  $s^0(c_{4,2})$ , the lower right sub-sub-quadrant of the lower left sub-quadrant, is now also significant. As a result, nodes 44, 54, 62, 66, and 72 are labeled with a one and nodes 46-52, 56-60, 64, 68, 70, and 74 are labeled with a zero. The final tree still has root node 26, branch nodes 28 and 36-42, and leaf nodes 44-74 and 30-34. As above, the values for bits  $b^0$  for coefficients  $c_{1,1}$ ,  $c_{1,4}$ ,  $c_{3,2}$ , and  $c_{4,3}$  have been noted under nodes 44, 54, 62, and 72, respectively, for future reference when coding the refinement information.

In FIG. 8C is depicted a table illustrating the encoding of the significance information and the refinement information for FIGS. 8A and 8B. Because below the highest bit-plane only the zero labeled nodes of the next higher tree are of interest, in this case, only nodes 46-52, 56-60, 64-70, 74, and 30-34 of FIG. 7B are of interest for coding significance information for FIG. 8B. Describing the significance information from left to right and in a bottom-up-depth-first fashion, nodes 46-52, 56-60, 64, 68, 70, 74, and 30-34 remain unchanged. Node 66 changes to being significant. The resulting order of the description is 46-52, 56-60, 64-70, 74, and 30-34. The resulting significance information  $\Delta_0$  is shown in the table. In this case, the values for bits  $b^0$  for coefficients  $c_{1,1}$ ,  $c_{1,4}$ ,  $c_{3,2}$ , and  $c_{4,3}$  make up refinement information  $R_0$  as shown in the table. Accordingly, after encoding all of the bit-planes of the coefficients from FIG. 3, the SNR scalable bit-stream consists of  $\Delta_3 R_3 \Delta_2 R_2 \Delta_1 R_1 \Delta_0 R_0$ .

Although presented in the context of compression of image data, one of ordinary skill in the art will realize that the method described above will apply to any coding of the bit-planes of an array of numbers. In addition, such skilled persons will realize that the encoding of significance information  $\Delta_n$  and refinement information  $R_n$  may be further compressed using Huffman coding or context based arithmetic coding. While the invention has been

illustrated and described by means of specific embodiments, it is to be understood that numerous changes and modifications may be made therein without departing from the spirit and scope of the invention as defined in the appended claims and equivalents thereof.

#### **CLAIMS**

#### What is claimed is:

1. A method of coding the bit-planes of an array of numbers comprising the steps

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converting the values in the array of numbers to binary;

determining the number of bit-planes based on a number having the maximum absolute value of the array of numbers;

generating a tree structured description of significance information for each bit-plane of the array based on a modified quad-tree coding technique;

generating an SNR scalable encoding of the significance information for each bitplane by describing new branches and leaves of the tree corresponding to each bit-plane in a bottom-up-depth-first manner;

generating an encoding of refinement information for each bit-plane; and generating a SNR scalable description of the array by concatenating the encoding of the significance information and the refinement information generated for each bit-plane.

- 2. The method according to claim 1 further comprising the step of truncating the binary values to a predetermined level of accuracy after the step of converting.
- A method of coding the bit-planes of an array of numbers, wherein the values
   in the array of numbers have been converted to binary, the binary values have been truncated
   to a predetermined level of accuracy, and the number of bit-planes has been determined based

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- on a number having the maximum absolute value of the array of numbers, the method comprising the steps of:
  - generating a tree structured description of significance information for each bit-plane of the array based on a modified quad-tree coding technique;
  - generating an SNR scalable encoding of the significance information for each bitplane by describing new branches and leaves of the tree corresponding to each bit-plane in a bottom-up-depth-first manner;

generating an encoding of refinement information for each bit-plane; and generating a SNR scalable description of the array by concatenating the encoding of the significance information and the refinement information generated for each bit-plane.

- 4. An apparatus for coding the bit-planes of an array of numbers comprising: means for converting the values in the array of numbers to binary; means for truncating the binary values to a predetermined level of accuracy;
- means for determining the number of bit-planes based on a number having the maximum absolute value of the array of numbers;
- 6 means for generating a tree structured description of significance information for each bit-plane of the array based on a modified quad-tree coding technique;
  - means for generating an SNR scalable encoding of the significance information for each bit-plane by describing new branches and leaves of the tree corresponding to each bit-plane in a bottom-up-depth-first manner;

means for generating an encoding of refinement information for each bit-plane; and

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- means for generating a SNR scalable description of the array by concatenating the encoding of the significance information and the refinement information generated for each bit-plane.
  - 5. The apparatus according to claim 4 further comprising means for truncating2 the binary values to a predetermined level of accuracy.
    - 6. A apparatus for coding the bit-planes of an array of numbers, wherein the values in the array of numbers have been converted to binary, the binary values have been truncated to a predetermined level of accuracy, and the number of bit-planes has been determined based on a number having the maximum absolute value of the array of numbers, the apparatus comprising:

means for generating a tree structured description of significance information for each bit-plane of the array based on a modified quad-tree coding technique;

means for generating an SNR scalable encoding of the significance information for each bit-plane by describing new branches and leaves of the tree corresponding to each bit-plane in a bottom-up-depth-first manner;

means for generating an encoding of refinement information for each bit-plane; and means for generating a SNR scalable description of the array by concatenating the encoding of the significance information and the refinement information generated for each bit-plane.

- A computer coding system for an input image, the system having a sampling 7. filter which decomposes the input image into four frequency subbands and outputs a Wavelet 2 transform, the system comprising:
- an encoder which generates a SNR scalable description of the Wavelet transform by 4 concatenating an encoding of significance information and an encoding of refinement
- information generated for each bit-plane, wherein the encoding of the significance 6 information for each bit-plane is generated by describing new branches and leaves of the tree 1953455 2 2 corresponding to each bit-plane in a bottom-up-depth-first manner.
  - The system according to claim 7 further comprising a decoder which utilizes 8. the SNR scalable description of the Wavelet transform to produce an output image.

# APPARATUS FOR AND METHOD OF SNR SCALABLE QUAD-TREE BIT-PLANE CODING

#### ABSTRACT OF THE DISCLOSURE

An apparatus for and a method of coding the bit-planes of an array of numbers. The values in the array of numbers are converted to binary. The binary values are truncated to a predetermined level of accuracy. The number of bit-planes is determined based on the largest absolute value. A tree structured description of the array is generated based on a modified quad-tree coding technique. An SNR (signal to noise ratio) scalable encoding of the significance information for each bit-plane is generated by describing new branches and leaves of the tree corresponding to each bit-plane in a bottom-up-depth-first manner. An encoding of refinement information for each bit-plane is generated. A SNR scalable description of the array is generated by concatenating the encoding of the significance information and the refinement information generated for each bit-plane.

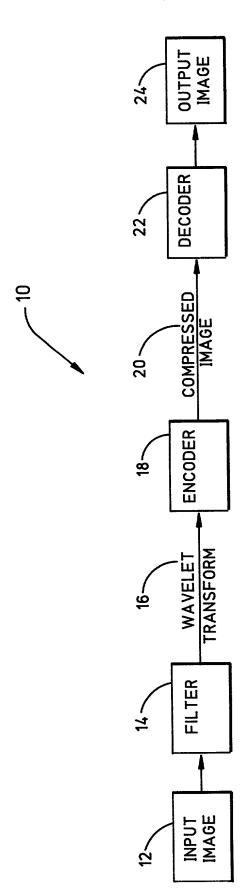


FIG.

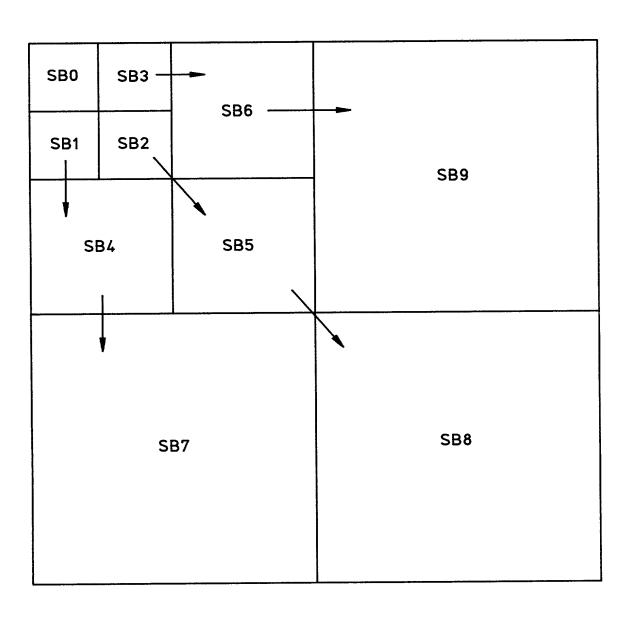


FIG. 2

		1	2	3	4	5	6	7	8
C <sub>i,j</sub>	1	10	0	0	6	0	0	0	0
	2	0	0	0	0	0	0	0	0
	3	0	3	0	0	0	0	0	0
	4	0	1	-9	0	0	0	0	0
	5	0	0	0	0	0	0	0	0
	6	0	0	0	0	0	0	0	0
	7	0	0	0	0	0	0	0	0
	8	0	0	0	0	0	0	0	0

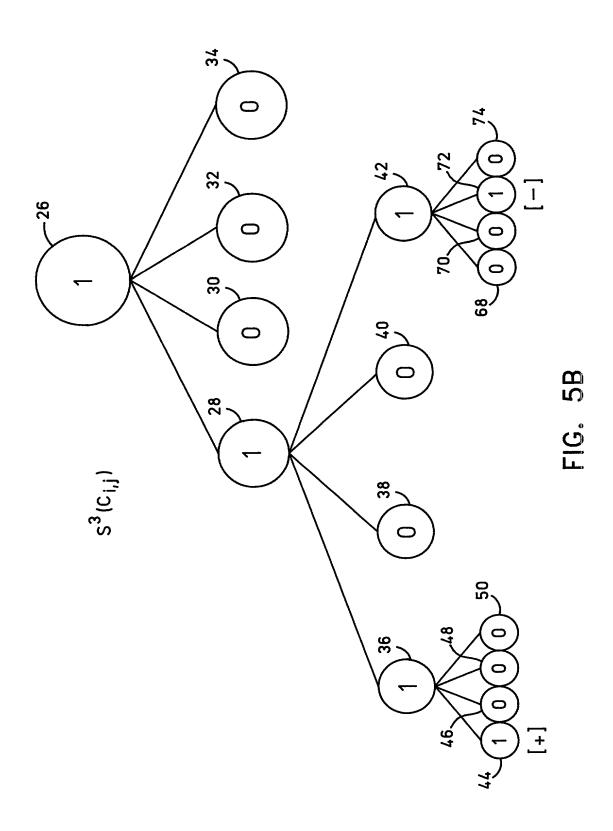
FIG. 3

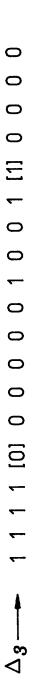
		-							
DECIMAL		BINARY							
DECIMAL	SGN	р 3	b <sup>2</sup>	b 1	o b				
C <sub>1,1</sub> =10		1	0	1	0				
C <sub>1,4</sub> =6		0	1	1	0				
C <sub>3,2</sub> =3		0	0	1	1				
C <sub>4,2</sub> =1		0	0	0	1				
C <sub>4,3</sub> =-9	_	1	0	0	1				

FIG. 4

		1	2	3	4	5	6	7	8
s <sup>3</sup> (C <sub>i,j</sub> )	1	1	0	0	0	0	0	0	0
	2	0	0	0	0	0	0	0	0
	3	0	0	0	0	0	0	0	0
	4	0	0	1	0	0	0	0	0
	5	0	0	0	0	0	0	0	0
	6	0	0	0	0	0	0	0	0
	7	0	0	0	0	0	0	0	0
	8	0	0	0	0	0	0	0	0

FIG. 5A



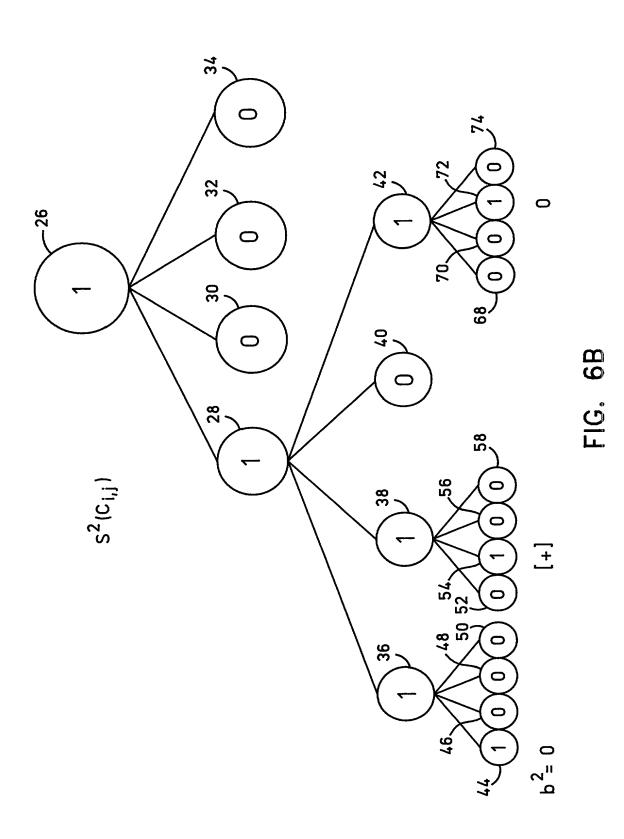


 $R_3$ 

FIG. 5C

		1	2	3	4	5	6	7	8
s <sup>2</sup> (C <sub>i,j</sub> )	1	1	0	0	1	0	0	0	0
	2	0	0	0	0	0	0	0	0
	3	0	0	0	0	0	0	0	0
	4	0	0	1	0	0	0	0	0
	5	0	0	0	0	0	0	0	0
	6	0	0	0	0	0	0	0	0
	7	0	0	0	0	0	0	0	0
	8	0	0	0	0	0	0	0	0

FIG. 6A

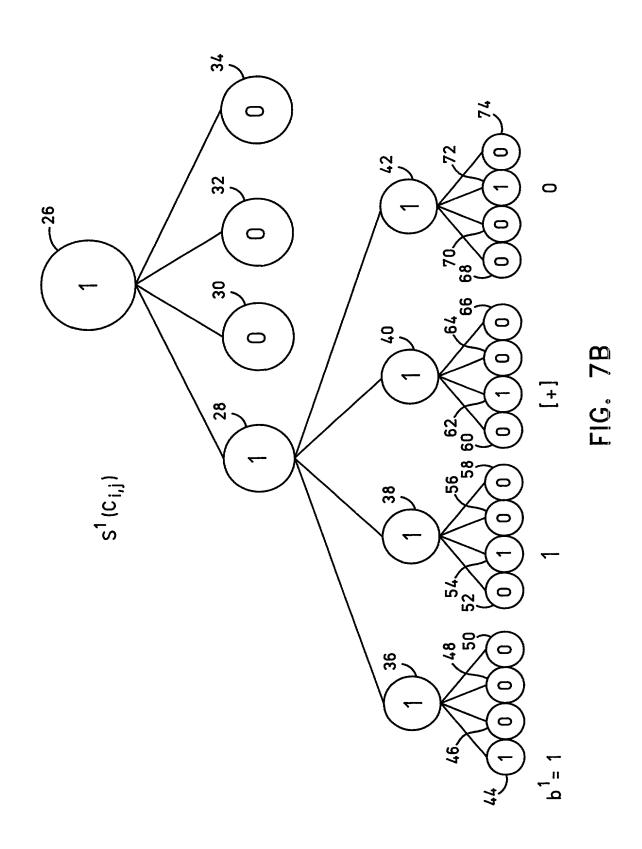




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		1	2	3	4	5	6	7	8
s <sup>1</sup> (C <sub>i,j</sub> )	1	1	0	0	1	0	0	0	0
	2	0	0	0	0	0	0	0	0
	3	0	1	0	0	0	0	0	0
	4	0	0	1	0	0	0	0	0
	5	0	0	0	0	0	0	0	0
	6	0	0	0	0	0	0	0	0
	7	0	0	0	0	0	0	0	0
	8	0	0	0	0	0	0	0	0

FIG. 7A

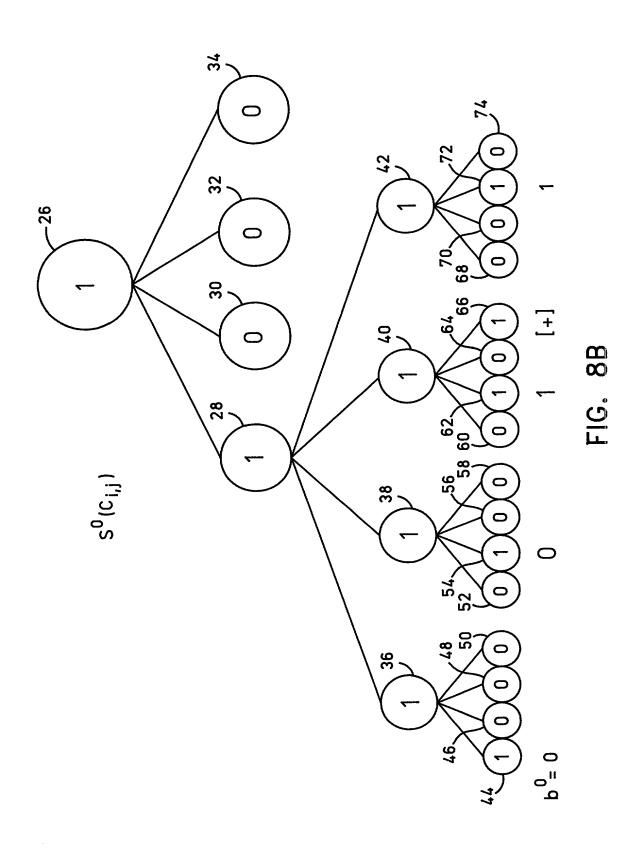


 $R_1 - 110$ 

FIG. 7C

		1	2	3	4	5	6	7	8
s <sup>0</sup> (C <sub>i,j</sub> )	1	1	0	0	1	0	0	0	0
	2	0	0	0	0	0	0	0	0
	3	0	1	0	0	0	0	0	0
	4	0	1	1	0	0	0	0	0
	5	0	0	0	0	0	0	0	0
	6	0	0	0	0	0	0	0	0
	7	0	0	0	0	0	0	0	0
	8	0	0	0	0	0	0	0	0

FIG. 8A





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#### **DECLARATION AND POWER OF ATTORNEY** FOR PATENT APPLICATION

ATTORNEY DOCKET NO. 10990265-1

As a below named inventor, I hereby declare that:

My residence/post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

APPARATUS FOR AND METHOD	OF SNR SCALABLE QUAD-TREE BIT-PLANE CODING	

APPARATUS FOR AND	METH	OD OF SNR SCALABLE	QUAD-TREE BIT-PLA	ANE CODING
the specification of wh	ich is a	ttached hereto unless t	he following box is c	hecked:
( ) was filed on Number	and	as US Appli was amended on	ication Serial No. or F	PCT International Application applicable).
I hereby state that I h including the claims, a disclose all information	s amen	ded by any amendmer	nt(s) referred to above	e above-identified specification, ve. I acknowledge the duty to CFR 1.56.
Foreign Application(s) and/or	Claim of	Foreign Priority		
inventor(s) certificate listed l	below an		any foreign application for	any foreign application(s) for patent or patent or inventor(s) certificate having
COUNTRY		APPLICATION NUMBER	DATE FILED	PRIORITY CLAIMED UNDER 35 U.S C. 119
				YES: NO:
				YES. NO:
Provisional Application I hereby claim the benefit u below:	nder Title	e 35, United States Code Se	ction 119(e) of any Unite	d States provisional application(s) listed
	AF	PPLICATION SERIAL NUMBER	FILING DATE	
U. S. Priority Claim			100 of any Haira	Control and book and bolom and
insofar as the subject matte manner provided by the firs information as defined in Tit	r of each t paragra le 37, Co	of the claims of this application of Title 35, United States	ation is not disclosed in tl s Code Section 112, I acl ection 1.56(a) which occu	States application(s) listed below and ne prior United States application in the knowledge the duty to disclose materia irred between the filing date of the prio
APPLICATION SERIAL NUMB	BER	FILING DATE	STATUS	(patented/pending/abandoned)
1		1		

#### **POWER OF ATTORNEY:**

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

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See Attached Page 3 of 3 Send Correspondence to: HEWLETT-PACKARD COMPANY

Direct Telephone Calls To:

Marc P. Schuyler (650) 857-3359

Intellectual Property Administration P.O. Box 272400 Fort Collins, Colorado 80528-9599

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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### ATTORNEY DOCKET NO. 10990265-1

## DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION (continued)

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Residence:				-		
Post Office Address:						
Inventor's Signature						
inventor's Signature	Da	te				
Full Name of # 4 joint inventor			Cit	izenshi <u>p</u>	<u> </u>	
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Inventor's Signature		ite				
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1 03t Office Address.					,	
Inventor's Signature	Da	ate				
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Residence:						
Post Office Address:						
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Inventor's Signature	D:	ate				
Full Name of # 7 joint invento	r:		Cir	tizenshi <u>p</u>	):	
Residence:						
Post Office Address:						
Inventor's Signature		ate				
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Tost Office Address.						
Inventor's Signature		ate				

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**POWER OF ATTORNEY: [CONTINUED]**As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) listed below to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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